

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (Use several sheets if necessary) <div style="border: 1px solid black; border-radius: 50%; padding: 10px; width: fit-content; margin: auto;"> NOV 28 2007 PATENT & TRADEMARK OFFICE </div>		Attorney Docket No.: 8006-1026	Application No.: 10/700,451
		Applicant: Hidefumi NATSUME et al.	
		Filing Date: November 5, 2003	Group Art Unit: 2619

U.S. PATENT DOCUMENTS						
Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing date (if appropriate)

FOREIGN PATENT DOCUMENTS							
Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
RH	JP 11-331237	11/30/1999	JAPAN (with English abstract)				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)		
		HIDAKA et al., "Proposal of Next Generation Ethernet Architecture: GOE, Collected Papers for Presentations 2 at 2002 Communications Society Conference by The Institute of Electronics, Information and Communication Engineers, B-7-11, August 20, 2002, pp. 171.
		MIYASAKA, "Dynamic VLAN System Using Tag Conversion Equipment", Collected Papers for Presentations 2 at 2002 General Conference by The Institute of Electronics, Information and Communication Engineers, B-6-74, March 7, 2002, pp. 88.

EXAMINER: 	DATE CONSIDERED <div style="font-size: 1.5em; font-family: cursive;">2/29/08</div>
---------------	---

EXAMINER. Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.